



2183

30 #9
8-17-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al. Art Unit: 2183
Serial No.: 09/387,109 Examiner:
Filed : August 31, 1999
Assignee : Intel Corporation
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AUG 13 2004

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

August 10, 2004

Date of Deposit

Signature

Jennifer H. Payne

Typed or Printed Name of Person Signing Certificate

Applicant : Matthew J. Adiletta, et al.
Serial No.: 09/387,109
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Attorney's Docket No.:10559-079001

Examiner's initials in the left column per MPEP 609 are
earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit
Account No. 06-1050.

Respectfully submitted,

Date: _____

8/10/04

Scott C. Harris

Reg. No. 32,030

Attorney for Intel Corporation

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